

	<b>L #</b>	<b>Hits</b>	<b>S arch T xt</b>	<b>DB</b>
<b>1</b>	<b>L1</b>	<b>1014</b>	<b>(361/761,762,763,764).c cls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>1118</b>	<b>(361/792,793,794,795).c cls.</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>3672</b>	<b>(343/700MS,702,873).cc ls.</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>65</b>	<b>(257/e27.113,e27.114).c cls.</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>5710</b>	<b>1 2 3 4</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>119</b>	<b>5 and @pd&gt;=20030519</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L7</b>	<b>13</b>	<b>6 and ((silicon polysilicon (gallium adj arsenide) si gaas semiconductor (integrated adj circuit\$1)) with layer\$2)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hit</b>	<b>S arch Text</b>	<b>DB</b>
<b>8</b>	<b>L8</b>	<b>106</b>	<b>6 not 7</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L9</b>	<b>93</b>	<b>8 and (capacitor\$1 condenser\$1 antenna\$1 aerial\$1 coil\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>13</b>	<b>8 not 9</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L11</b>	<b>1284</b>	<b>(257/728,724).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>12</b>	<b>L12</b>	<b>24</b>	<b>11 and @pd&gt;=20030519</b>	<b>USP AT; US-P GPU B</b>
<b>13</b>	<b>L13</b>	<b>842</b>	<b>257/700.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>14</b>	<b>L14</b>	<b>801</b>	<b>(361/780,782,783).ccls.</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>S arch T xt</b>	<b>DBs</b>
<b>15</b>	<b>L15</b>	<b>1601</b>	<b>13 14</b>	<b>USP AT; US-P GPU B</b>
<b>16</b>	<b>L16</b>	<b>799</b>	<b>15 and ((silicon polysilicon (gallium adj arsenide) si gaas semiconductor (integrated adj circuit\$1)) with layer\$2)</b>	<b>USP AT; US-P GPU B</b>
<b>17</b>	<b>L17</b>	<b>299</b>	<b>16 and (capacitor\$1 condenser\$1 antenna\$1 aerial\$1 coil\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>18</b>	<b>L18</b>	<b>295</b>	<b>17 not (6 12)</b>	<b>USP AT; US-P GPU B</b>
<b>19</b>	<b>L19</b>	<b>684</b>	<b>(257/759,760).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>20</b>	<b>L20</b>	<b>166</b>	<b>19 and (capacitor\$1 condenser\$1 antenna\$1 aerial\$1 coil\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>21</b>	<b>L21</b>	<b>154</b>	<b>20 not (6 12 18)</b>	<b>USP AT; US-P GPU B</b>